A **superscalar processor** is a [CPU](https://en.wikipedia.org/wiki/Central_processing_unit) that implements a form of [parallelism](https://en.wikipedia.org/wiki/Parallel_computer) called [instruction-level parallelism](https://en.wikipedia.org/wiki/Instruction-level_parallelism) within a single processor. In contrast to a [scalar processor](https://en.wikipedia.org/wiki/Scalar_processor) that can execute at most one single instruction per clock cycle, a superscalar processor can execute more than one instruction during a clock cycle by simultaneously dispatching multiple instructions to different [execution units](https://en.wikipedia.org/wiki/Execution_unit) on the processor.